



ATTORNEY DOCKET 728-236 (YOR9 2003 0149 US1)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sameh W. Asaad et al. Examiner:
Serial No.: 10/620,734 Group Art Unit: 2183
Filed: July 16, 2003 Date: March 16, 2004
For: **SYSTEM AND METHOD FOR INSTRUCTION MEMORY
STORAGE AND PROCESSING BASED ON BACKWARDS
BRANCH CONTROL INFORMATION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

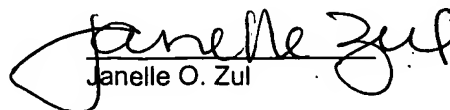
Pursuant to Applicant(s) duty of disclosure, the information listed on the attached form PTO-1449 is brought to the attention of the Examiner. A copy of the listed references are enclosed.

The citation of the listed items is not a representation that they constitute a complete or exhaustive listing of the relevant art or that the references are prior art. The items listed are submitted in good faith, but are not intended to a substitute for the Examiner's search. It is hoped, however, that in addition to apprising the Examiner of these particular items, they will assist in identifying fields of search and in making as full and complete a search as possible.

CERTIFICATION UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: March 16, 2004


Janelle O. Zul

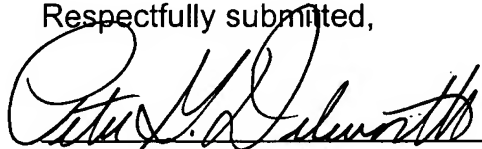
The filing of this information disclosure statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

- ☐ This information disclosure statement is being filed within three (3) months of the filing date of this application.
- ☐ This information disclosure statement is being filed within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application.
- ☒ To the best of Applicant(s) knowledge, this information disclosure statement is being filed before the date of mailing of a first Office Action on the merits in connection with this case.
- ☐ Enclosed herewith is a certificate under 37 C.F.R. § 1.97(e)(1).
- ☐ Enclosed herewith is a petition under 37 C.F.R. § 1.97(d)(ii).
- ☐ Enclosed by check is the petition fee of \$130.00. 37 C.F.R. § 1.17(i)(1))
- ☐ Please charge the **\$130.00** petition fee to Deposit Account No. **05-0510 IBM**.
- ☐ Enclosed by check is the **\$180.00** fee required by 37 C.F.R. § 1.17(p).
- ☐ Please charge the **\$180.00** fee required by 37 C.F.R. § 1.17(p) to Deposit Account No. **05-0510 IBM**.

[X] Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. § 1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account 05-0510 IBM. Also, in the event any extensions of time for responding are required for the pending application(s), please treat this paper as a petition to extend the time as required and charge Deposit Account No. 05-0510 IBM therefor. **TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.**

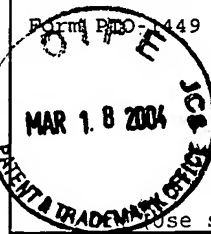
Early and favorable consideration of the case is respectfully requested.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Peter G. Dilworth", is written over a horizontal line.

Peter G. Dilworth
Reg. No. 26,450
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Form PTO-1449 		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 728-236 (YOR9 2003 0149 US)		SERIAL NO. 10/620,734	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				APPLICANTS Sameh W. Asaad et al.			
FILING DATE July 16, 2003				GROUP ART UNIT 2183			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,401,196	06/04/02	Lee et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
		1.	N. Bellas, I. Hajj and C. Polychronopoulos, in "A New Scheme for I-Cache Energy Reduction In High-Performance Processors," Power Driven Microarchitecture Workshop, held in conjunction with ISCA 98, Barcelona, Spain, June 28th 1998.				
		2.	K. Ghose and M. B. Kamble, in "Energy Efficient Cache Organizations for Superscalar Processors," Power Driven Microarchitecture Workshop, held in conjunction with ISCA 98, Barcelona, Spain, June 28th 1998.				
		3.	J. Kin, M. Gupta and W. Mangione-Smith, "The Filter Cache: An Energy Efficient Memory Structure," Proc. Int'l Symposium on Microarchitecture, pp. 184-193, December, 1997				
		4.	C. Su, A. M. Despaigne, in "Cache Design Tradeoffs for Power and Performance Optimization: A Case Study," Proc. Int'l Symposium on Low Power Design, pp. 63-68, 1995.				
		5.	L.H. Lee, W. Moyer and J. Arends, "Instruction Fetch Energy Reduction Using Loop Caches for Embedded Applications with Small Tight Loops," Proc. Int'l Symp on Low Power Design, 1999.				
EXAMINER				DATE CONSIDERED			
* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							